Notice of Allowability	Application No.	Applicant(s)
	10/686,331	BUTLER, RICHARD M.
	Examiner	Art Unit
	CHAT C. DO	2193
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The MAILING DATE of this communication appeal all claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in the or other appropriate communicement. This application is subsected by the community of the communit	his application. If not included ication will be mailed in due course. THIS
1. This communication is responsive to <u>02/20/2008 and 01/14/2008</u> .		
2. X The allowed claim(s) is/are 1-3 and 7-22, as now renumbered as 1-19.		
 3.		
2. ☐ Certified copies of the priority documents have been received in Application No		
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) hereto or 2) to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s)	5 D Notice of Infe	manal Datant Annillantian
 Notice of References Cited (PTO-892) D Notice of Draftperson's Patent Drawing Review (PTO-948) 	6. ☑ Interview Sun	rmal Patent Application
3. ☐ Information Disclosure Statements (PTO/SB/08),	Paper No./M	ail Date <u>attached herein</u> . mendment/Comment
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit	8. 🗌 Examiner's St	tatement of Reasons for Allowance
of Biological Material	9.	
	/Chat C. Do/ Primary Examiner, A	rt Unit 2193

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EXAMINER'S AMENDMENT

1. Claims 4-6 are cancelled.

- 2. Claims 1-3 and 7-22 are allowed and renumbered as 1-19.
- 3. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Kyle Way, Reg. No. 45,549, on 04/14/2008.

The application has been cancelled claims 4-6 and amended claim 1 as follows:

1. (Currently Amendment) A method of generating a random number, comprising:

sampling data transmitted over a number of microprocessor buses at inputs of a number of multiple input shift registers (MISRs) coupled with the number of microprocessor buses;

generating values within the MISRs based on the sampled data;

retrieving the values from the number of MISRs; and

generating a random number which is based on the values retrieved from the number of MISRs;

wherein the inputs of at least one of the number of MISRs are coupled to at least one of a data address bus which transfers data addresses between a data address cache and a CPU store, an instruction data bus which transfers instructions between an instruction data cache and a CPU core, and an instruction address bus which transfers instruction addresses between an instruction addresses between an instruction addresses cache and a CPU core.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to CHAT C. DO whose telephone number is (571)272-3721. The examiner can normally be reached on Tue-Fri 9:00AM to 7:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lewis Bullock can be reached on (571) 272-3759. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Chat C. Do/ Primary Examiner, Art Unit 2193

April 26, 2008